UTILITY PATENT APPLICATION TRANSMITTAL

(Only for new nonprovisional applications under 37 CFR 1.53(b))

Attorney Docket No.	35.C13942
First Named Inventor or Application Identifier	HIROYUKI SAITO
Express Mail Label No.	

Jc511 U.S. PTO 09/425225 10/22/99

Jc688 U.S. PTO 10/22/99

APPLICATION ELEMENTS

See MPEP chapter 600 concerning utility patent application contents.

ADDRESS TO: Assistant Commissioner for Patents
Box Patent Application
Washington, DC 20231

1. [X] Fee Transmittal Form
(Submit an original, and a duplicate for fee processing)

2. [X] Specification — *Total Pages* 24

3. [X] Drawing(s) *(35 USC 113)* — *Total Sheets* 10

4. [X] Oath or Declaration — *Total Pages* 1

   a. [X] Newly executed (original or copy)
   b. [ ] Unexecuted for information purposes
   c. [ ] Copy from a prior application (37 CFR 1.63(d))
   (for continuation/divisional with Box 17 completed)
   [Note Box 5 below]
   i. [ ] DELETION OF INVENTOR(S)
   Signed Statement attached deleting inventor(s) named in the prior application, see 37 CFR 1.63(d)(2) and 1.33(b).

5. [ ] Incorporation By Reference *(useable if Box 4c is checked)*
The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied under Box 4c, is considered as being part of the disclosure of the accompanying application and is hereby incorporated by reference therein.

6. [ ] Microfiche Computer Program *(Appendix)*

7. Nucleotide and/or Amino Acid Sequence Submission
(if applicable, all necessary)
   a. [ ] Computer Readable Copy
   b. [ ] Paper Copy (identical to computer copy)
   c. [ ] Statement verifying identity of above copies

ACCOMPANYING APPLICATION PARTS

8. [X] Assignment Papers (cover sheet & document(s))

9. [ ] 37 CFR 3.73(b) Statement *(when there is an assignee)* [ ] Power of Attorney

10. [ ] English Translation Document *(if applicable)*

11. [ ] Information Disclosure Statement (IDS)/PTO-1449 [ ] Copies of IDS Citations

12. [X] Preliminary Amendment

13. [X] Return Receipt Postcard (MPEP 503)
(Should be specifically itemized)

14. [ ] Small Entity Statement(s) [ ] Statement filed in prior application Status still proper and desired

15. [ ] Certified Copy of Priority Document(s)
(if foreign priority is claimed)

16. [ ] Other: _____

17. If a CONTINUING APPLICATION, *check appropriate box and supply the requisite information:*
[ ] Continuation [ ] Divisional [ ] Continuation-in-part (CIP) of prior application No. _____

18. CORRESPONDENCE ADDRESS

[X] Customer Number or Bar Code Label — 05514 (Insert Customer No. or Attach bar code label here) — or [ ] Correspondence address below

NAME					
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City		State		Zip Code	
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CLAIMS	(1) FOR	(2) NUMBER FILED	(3) NUMBER EXTRA	(4) RATE	(5) CALCULATIONS
	TOTAL CLAIMS (37 CFR 1.16(c))	41-20 =	21	X $ 18.00 =	$ 378.00
	INDEPENDENT CLAIMS (37 cfr 1.16(b))	7-3 =	4	X $ 78.00 =	$ 312.00
	MULTIPLE DEPENDENT CLAIMS (if applicable) (37 CFR 1.16(d))			$ 260.00 =	$ 260.00
				BASIC FEE (37 CFR 1.16(a))	$ 760.00
			Total of above Calculations =		$1710.00
	Reduction by 50% for filing by small entity (Note 37 CFR 1.9, 1.27, 1.28).				
				TOTAL =	~~$2160.00~~ 1710

19. Small entity status
    - a. ☐ A Small entity statement is enclosed
    - b. ☐ A small entity statement was filed in the prior nonprovisional application and such status is still proper and desired.
    - c. ☐ Is no longer claimed.

20. ☒ A check in the amount of $ 1710.00 to cover the filing fee is enclosed.

21. ☒ A check in the amount of $ 40.00 to cover the recordal fee is enclosed.

22. The Commissioner is hereby authorized to credit overpayments or charge the following fees to Deposit Account No. 06-1205:
    - a. ☒ Fees required under 37 CFR 1.16.
    - b. ☐ Fees required under 37 CFR 1.17.
    - c. ☐ Fees required under 37 CFR 1.18.

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT REQUIRED	
NAME	Gary M. Jacobs - Reg. No. 28,861
SIGNATURE	
DATE	October 22, 1999

GMJ\vjw

35.C13942 PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:	)	
	:	Examiner: Not yet assigned
Hiroyuki Saito	)	
	:	Group Art Unit: Not yet assigned
Application No.: Not yet assigned	)	
	:	
Filed: October 22, 1999	)	
	:	
For: Recording Apparatus	)	October 22, 1999

Assistant Commissioner for Patents
Washington, D.C. 20231

PRELIMINARY AMENDMENT

Sir:

Prior to examination, please amend the application as follows:

IN THE CLAIMS

Please amend Claim 11as follows:

Claim 11

Line 2, change "claims 1 to 10" to --claims 1-5--.

Remarks

Claim 11 has been amended to correct the dependency thereof so that it is a proper multiple dependent claim. Claims 1-17 are pending in the application. Claims 1, 2, 4, 5, 12, 16, and 17 are independent.

Consideration and an early allowance are respectfully solicited.

Applicants' undersigned attorney may be reached in our Washington D.C. office by telephone at (202) 530-1010. All correspondence should continue to be directed to our below listed address.

Respectfully submitted,

Attorney for Applicants
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09/25/2003 15:02:01

RECORDING APPARATUS

BACKGROUND OF THE INVENTION

Field of the Invention

The present invention relates to a recording apparatus having a stepping motor as an actuator, particularly to a recording apparatus equipped with a sleep mode for suppressing power consumption at the time of software power off.

Related Background Art

In recent years, with an increasing demand for reduction of power consumption, a machine provided with a sleep mode has been developed in which an unnecessary circuit is not operated in a software power off state, and CPU clock is further lowered to suppress the power consumption. Additionally, there has also been a machine in which only a pilot lamp for informing a user is turned off even in the software power off state. The machine is on standby while the power consumption is substantially unchanged. However, this cannot be assumed to be placed in the sleep mode.

In a recording apparatus using a stepping motor as an actuator, even when the device is started from either a hardware power on state or from a sleep mode, a motor mechanical phase (angle) (rotor position) is not seen. Therefore, to equalize a motor electrical phase (angle) (exciting phase) with the mechanical

phase, pulses for one or more cycles are inputted at a low frequency within an automatic starting area and in at least the electrical phase to perform phase alignment.

The states of the electrical and mechanical phases during starting are shown in Figs. 10A to 10D. In the drawings, for the description, it is assumed that the motor is driven in two-phase excitation and stopped in two phase positions without considering any detent. An arrow indicates the electrical phase (exciting phase), and v indicates the mechanical phase. In Fig. 10A, since the electrical phase is equal to the mechanical phase, the device smoothly starts up without causing any positional deviation. In Figs. 10B and 10D, however, since the electrical phase deviates from the mechanical phase by 90 degrees, positional deviation occurs by this phase difference during starting. Furthermore, when through-up occurs excessively steeply, loss of synchronism occurs in worst cases. In Fig. 10C, since the phase difference is 180 degrees, there is a high possibility that not only the positional deviation but also the loss of synchronism occurs. To avoid the worst situation of loss of synchronism, as described above, the phase alignment has been performed which comprises inputting the pulses for one or more cycles at the low frequency within the automatic starting area in which there is a sufficient

torque and in at least the electrical phase to equalize the electrical phase with the mechanical phase.

In the conventional method, however, when the electrical and mechanical phases of the stepping motor are actually different from each other, a targeted effect can be obtained. However, the phase alignment is performed even during starting from the state of Fig. 10A (the electrical phase is equal to the mechanical phase). Therefore, when some pulses of low frequencies are inputted in the automatic starting area where there is a sufficient torque, noise or slight vibration is unfavorably generated.

SUMMARY OF THE INVENTION

The present invention has been developed in consideration of the above-described actual circumstances, and an object thereof is to drive a stepping motor for use in a recording apparatus in an optimum state.

Another object of the present invention is not to perform phase alignment when the phase of the stepping motor is aligned during restarting, and to perform the phase alignment when the phase of the stepping motor is not aligned.

Further objects of the present invention would be apparent from concrete embodiments described below.

BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 is a perspective view best showing the characteristics of a recording apparatus according to a first embodiment of the present invention.

Fig. 2 is a circuit block diagram of a controller according to the first embodiment of the present invention.

Fig. 3 is a flowchart showing an operation procedure according to the first embodiment of the present invention.

Fig. 4 is a perspective view best showing the characteristics of the recording apparatus according to a second embodiment of the present invention.

Fig. 5 is a circuit block diagram of the controller according to the second embodiment of the present invention.

Fig. 6 is a flowchart showing the operation procedure according to the second embodiment of the present invention.

Fig. 7 is a perspective view best showing the characteristics of the recording apparatus according to a third embodiment of the present invention.

Fig. 8 is a circuit block diagram of the controller according to the third embodiment of the present invention.

Fig. 9 is a flowchart showing the operation procedure according to the third embodiment of the

present invention.

Figs. 10A, 10B, 10C and 10D are schematic diagrams showing the states of a mechanical phase (angle) and an electrical phase (angle) of a stepping motor.

DESCRIPTION OF THE PREFERRED EMBODIMENTS

Embodiments of the present invention will be described hereinafter with reference to the drawings.

(First Embodiment)

In a first embodiment, a serial ink jet printer provided with a recording head with an ink tank attached thereto will be described as an example. Fig. 1 is a schematic view of the serial ink jet printer showing the mechanism of the present invention. In Fig. 1, numeral 101 denotes a carriage which has an ink tank and also serves as a recording head. A bearing 101a fixed to the carriage 101 is impregnated with lubricating oil, a guide shaft 102 is inserted slidably in a main scanning direction, and both ends of the guide shaft 102 are fixed to a chassis 103. Drive of a carriage drive motor (hereinafter referred to as CR motor) 105 is transmitted via a belt 104 as carriage drive transfer means engaged with the carriage 101, so that the carriage 101 can move in the main scanning direction. Here, an idler pulley 106 is disposed on the side opposite to the CR motor 105 via the belt 104.

When a printing material 111 is on standby for

printing, it is stacked on sheet supply means 110. When the printing starts, the printing material 111 is supplied by pickup means (not shown). Thereafter, in synchronism with the reciprocating movement of the carriage 101, the printing material 111 is conveyed by a conveying roller 107 by an appropriate feed amount in a sub-scanning direction at an appropriate time, and the printing is performed. The conveying roller 107 is rotated/moved by the drive force of a conveying motor (hereinafter referred to as LF motor) 109 via a press-inserted conveying drive gear 108. After the printing is completed, the printing material 111 is discharged by the conveying roller 107 and discharging means (not shown).

Here, the drive of the carriage motor 105, LF motor 109, pickup means (not shown), and recording head in the carriage 101 is controlled by a controller 112.

Fig. 2 is a block diagram of the controller 112. The controller is provided with a power supply 214 for operating the recording apparatus, a CPU 201 as a central processing calculation circuit, and a gate array 202. The image data transmitted from an interface is developed and processed in DRAM 210, the CR motor 105 and the LF motor 109 are driven by drivers 207 and 208, respectively, and a head 209 is controlled via a head driver 206 to perform the printing. A program for controlling the printer is stored in ROM

211, and the CPU 201 and the gate array 202 operate under program instructions. Numeral 212 is an EE-PROM for holding written information even in a hardware power off state, in which printer status information such as the number of accumulated/printed sheets are stored. A SRAM 203 is disposed inside the CPU 201, in which stored are CR phase data 204 as the phase data of CR motor 105 and LF motor, LF phase data 205, and termination status data 213 indicating whether the printer is normally or erroneously terminated at the time of software power off. The SRAM does not necessarily exist inside the CPU, and may be any memory as long as storage can be kept even in a sleep mode.

To reduce the power consumption, when the user softly turns off power, the printer enters the sleep mode (the printer may automatically be placed into the sleep mode by counting continuous unused time, and the like by a timer), only the logic signal is enabled, the power supply to the head 209, the CR motor 105, and the LF motor 109 is cut, and clock down is further performed. In the sleep mode the RAM information other than the SRAM 203 mounted inside the CPU 201 are all deleted. The information of an exciting phase to stop the CR motor 105 and the LF motor 109 at the time of software power off are written in the CR phase data 204 and the LF phase data 205, respectively, and the presence/absence of an error is written into the

termination status data 213 at the time of software power off.

A procedure for returning from the sleep mode will next be described with reference to the flowchart of Fig. 3. When the user depresses a power on key (not shown), a returning sequence starts. At step S301 the termination status data 213 when entering the sleep mode is confirmed. In case of normal termination (step S302), the CR phase data 204 and the LF phase data 205 written in the SRAM 203 at the time of software power off are read (step S303). The read CR phase data 204 and the LF phase data 205 are set in the data area of start exciting phase (step S304), and the motors are started (step S305). Since the user does not perform the hardware power off or the error termination, a motor rotor is in the sleep state. A probability that a change occurs from the state at the time of software power off is close to zero. Therefore, when the electrical phase is equal to the mechanical phase (state of Fig. 10A), the software power off is performed. Even at the time of software power on, while the phases are equal to each other, the motor is started. Therefore, since no phase alignment needs to be performed, the drive in the low frequency area with a large torque can be minimized, and the starting with less noise or vibration is possible.

When the error termination is judged at step S302,

it can be considered that the error is generated because the motor comes out of step, and there is a high possibility that each motor mechanical phase is different from the stored motor phase data. Therefore, motor phase alignment is performed to align the mechanical and electrical phases (step S306). Additionally, since there is also a high possibility that the error occurs without any motor loss of synchronism, the start exciting phase of phase alignment is preferably also started from the phase data stored in the SRAM 203 in order to avoid the noise or vibration generated by the positional deviation during starting to the utmost.

As described above, by rising from the motor phase stored at the time of software power off, a quiet starting can be realized with less vibration.

In the embodiment since the phase data at the time of software power off is stored in the SRAM 203, the data is deleted at the time of hardware power off. Since movement or transport is considered to be mainly performed at the time of hardware power off, it is expected that the motor phase deviates, and motor phase alignment is needed. From this idea, it is judged that the phase data is unnecessary, but the phase data may be stored in non-volatile EE-PROM 212 kept even at the time of hardware power off.

Moreover, when the object motor is a stepping

motor, it can similarly be handled, and the present invention may be applied to printing material sheet supply means, head maintenance mechanism drive means, and the like (these means are not shown).

(Second Embodiment)

Fig. 4 is a schematic view of the serial ink jet printer showing the mechanism according to a second embodiment of the present invention, and the same reference numerals as those of the first embodiment provide the same elements, structures and functions unless otherwise described.

In Fig. 4, a CR sensor (photo sensor) 401 is mounted on the carriage 101 to judge the position of the carriage 101 by detecting whether a shielding plate 402 intercepts the CR sensor. The CR shielding plate 402 is disposed on a home position side (where the carriage is on standby in the sleep mode), and the CR sensor 401 is intercepted in the sleep state (OFF state). The CR sensor 401 returns from the sleep state, moves by the predetermined number of pulses (toward the right in Fig. 4), and transmits light (ON state).

Numeral 403 denotes an LF sensor (photo sensor) for detecting the rotating phase of the conveying roller, and judgment is made when an LF shielding plate 404 press-inserted to the conveying roller 107 intercepts the LF sensor. The LF sensor is on standby

in the sleep state while the LF shielding plate is intercepted in the home position (OFF state). The LF sensor 401 returns from the sleep state, moves by the predetermined number of pulses (by one cycle of the LF shielding plate), transmits light midway during rotation (ON state), and is again shielded (OFF state).

Fig. 5 is a block diagram of the controller 112 according to the second embodiment. Herein the same reference numerals as those of the first embodiment provide the same elements, structures and functions unless otherwise described. The states of CR sensor 401 and LF sensor 403 of Fig. 4 are monitored by the CPU 201.

In the same manner as in the first embodiment, the information of the exciting phase to stop the CR motor 105 and the LF motor 109 when entering the sleep mode, that is, at the time of software power off are written in the CR phase data 204 and the LF phase data 205, respectively.

The procedure for returning from the sleep state will be described with reference to the flowchart of Fig. 6. When the sequence starts, first the CR phase data 204 and the LF phase data 205 written at the time of software power off are read (step S601). The read CR phase data 204 and the LF phase data 205 are set in the data area of start exciting phase (step S602), and the motors are started. The CR motor 105 is driven to

a position where the CR sensor 401 is released from the CR shielding plate 402, and the LF motor 109 is driven by one cycle of the LF shielding plate 404 (Of course, judgment may be made by a smaller feed amount than one cycle by defining LF shielding area). The outputs of CR sensor 401 and LF sensor 403 are monitored midway during driving, and it is checked whether the motors are driven without any loss of synchronism (whether the CR sensor 401 leaves the CR shielding plate at the predetermined timing, whether the LF sensor 403 emits the corresponding output to the LF shielding plate) (step S603). When there is no abnormality in the drive check (step S604), the motor starting is completed. When an abnormality is detected (step S604), it is judged that the motor is out of step, and motor phase alignment is performed (step S606).

In the embodiment, not only when the motor mechanical phase coincides with the electrical phase (state of Fig. 10A) but also when there is an initial positional deviation recovery but there is no loss of synchronism (e.g., states of Fig. 10B, 10D), quiet starting can be realized without performing the phase alignment (a slight noise is made at the time of the initial positional deviation recovery, but it is quieter than at the time of phase alignment). Furthermore, it is confirmed whether the motor comes out of step (step S604), and the phase alignment then

follows (step S606). Therefore, there is neither loss of synchronism nor error termination during starting.

Moreover, the phase data may be stored in the EEPROM 212 not in the SRAM 203 also in the second embodiment.

Furthermore, when the object motor is a stepping motor, it can similarly be handled, and the present invention may be applied to the printing material sheet supply means, the head maintenance mechanism drive means, and the like (these means are not shown).

(Third Embodiment)

Fig. 7 is a schematic view of the serial ink jet printer showing the mechanism according to a third embodiment of the present invention, and the same reference numerals as those of the first embodiment provide the same elements, structures and functions unless otherwise described.

In Fig. 7, an optical linear encoder scale 702 is read by CR encoder sensor 701 mounted on the carriage 101, and the position of the carriage 101 corresponding to the rotating amount of the CR motor 105 is monitored. An optical rotary encoder scale 704 is read by LF encoder sensor 703, and the rotating amount of the conveying roller 107 corresponding to the rotating amount of the LF motor 109 is monitored.

Fig. 8 is a block diagram of the controller 112 according to the third embodiment, and the same

reference numerals as those of the first embodiment provide the same elements, structures and functions unless otherwise described.

When the CPU 201 monitors the states of the CR encoder sensor 701 and LF encoder sensor 703 of Fig. 7, the carriage position and the conveying roller rotating position are grasped. CR positional data 801 and LF positional data 802 are data of carriage position and conveying roller rotating amount obtained from the CR encoder sensor 701 and the LF encoder sensor 703, respectively, and are stored in the SRAM 203.

In the same manner as in the first embodiment, the information of the exciting phase to stop the CR motor 105 and the LF motor 109 when entering the sleep mode, that is, at the time of software power off are written in the CR phase data 204 and the LF phase data 205, respectively. Additionally, the states of the CR encoder sensor 701 and the LF encoder sensor 703 are continuously monitored in the sleep state, and the positions (rotating amount) of the carriage 101 and the conveying roller are continuously written to the CR positional data 801 and the LF positional data 802.

The procedure for returning from the sleep state will be described with reference to the flowchart of Fig. 9. First, in the sleep state, the pulses of the CR encoder sensor 701 and the LF encoder sensor 703 are counted, and the CR positional data 801 and the LF

positional data 802 are continuously written to the SRAM 203 as described above (step S901). When a trigger of software power on is applied (step S902), the CR positional data 801 and the LF positional data 802 are converted to the exciting phase data of the CR motor 105 and the LF motor 109, respectively, and the CR phase data 204 and the LF phase data 205 compatible with the motor rotor position (mechanical phase) at that time are written to the SRAM 203 (step S903). The data are set in the data area of the start exciting phase (step S602), and the motors are started.

By the above-described procedure the positions (rotation) of the carriage 101 and the conveying roller 107 are always monitored, and excitation is performed from the exciting phase corresponding to the position (rotation amount) during the motor starting. Even when the carriage 101 and the conveying roller 107 move (rotate) in the sleep state, the positional deviation or the loss of synchronism in the initial starting stage is not generated (state of Fig. 10A), so that a quiet motor starting can constantly be realized.

The linear encoder is used for detecting the position of the carriage 101, but the rotary encoder may directly be attached to the CR motor 105 to directly monitor the motor phase, and the rotary encoder may similarly be attached to the LF motor.

Moreover, also in the third embodiment, the phase

data may be stored in the EE-PROM 212, not in the SRAM 201.

Furthermore, when the object motor is a stepping motor, it can similarly be handled, and the present invention may be applied to the printing material sheet supply means, the head maintenance mechanism drive means, and the like (these means are not shown).

As described above, in the recording apparatus having the stepping motor as the actuator, when the phase data at the time of motor stop is held in the sleep mode in which the power of the recording apparatus is softly turned off, and the motor is started up from the stored phase, the positional deviation generated during the phase alignment and the vibration and noise by the excessively large torque can be avoided. Moreover, only when there is a possibility that the motor phase deviates, the phase alignment is appropriately be performed. Therefore, the probability of the generation of vibration and noise can be reduced, and additionally a stable motor starting can be performed. Furthermore, by also using the encoder and other position detecting means and directly or indirectly monitoring the motor rotating amount in the sleep state, the motor starting can be realized without requiring the phase alignment.

WHAT IS CLAIMED IS:

1. A recording apparatus provided with a stepping motor as an actuator, comprising: storage means for storing and holding a final exciting phase of said motor at the time of software power off; and control means for starting excitation from said final exciting phase without performing phase alignment of said motor at the time of restarting from a software power off state.

2. A recording apparatus provided with a stepping motor as an actuator, comprising: storage means for storing and holding a final exciting phase of said motor at the time of software power off and a termination status indicating the presence/absence of an abnormality at the time of software power off; and control means for, at the time of restarting from a software power off state, starting excitation from said final exciting phase without performing phase alignment of said motor when the termination status is normal, and performing said phase alignment of the motor when the terminal status is abnormal.

3. The recording apparatus according to claim 2, wherein said control means starts the phase alignment of the motor from said final exciting phase when the termination status is abnormal.

4. A recording apparatus provided with a stepping motor as an actuator, comprising: storage means for storing and holding a final exciting phase of said motor at the time of software power off; a sensor for, when predetermined pulses are applied to said motor from a standby position, judging whether the motor moves by the predetermined pulses; and control means for starting excitation from said final exciting phase to apply the predetermined pulses without performing phase alignment of said motor at the time of restarting from a software power off state, normally starting when said sensor judges that the motor moves by said predetermined pulses, and performing the phase alignment of said motor when it is judged that the motor does not move by said predetermined pulses.

5. A recording apparatus provided with a stepping motor as an actuator, comprising: storage means for storing and holding a final exciting phase of said motor at the time of software power off; a sensor for detecting a rotating amount of said motor or an equivalent value which is detectable even in a software power off state; and control means for, at the time of restarting from the software power off state, obtaining an exciting phase compatible with a rotor position of said motor at the time of restarting from data of said rotating amount of the motor and said final exciting

phase detected by said sensor, and starting excitation from said obtained exciting phase without performing phase alignment of said motor.

6. The recording apparatus according to any one of claims 1 to 5, wherein said recording apparatus is a serial type recording apparatus.

7. The recording apparatus according to claim 6, wherein said stepping motor is a carriage driving motor.

8. The recording apparatus according to claim 6, wherein said stepping motor is a printing material conveying motor.

9. The recording apparatus according to claim 6, wherein said stepping motor is a printing material sheet supplying motor.

10. The recording apparatus according to claim 6, wherein said stepping motor is a recording head maintenance mechanism driving motor.

11. The recording apparatus according to any one of claims 1 to 10, wherein said recording apparatus is an ink jet type recording apparatus.

12. A recording apparatus provided with a stepping motor as an actuator, comprising:

drive means for changing an exciting phase of the stepping motor to step-drive the stepping motor;

storage means for storing a final exciting phase of said stepping motor at the time of power off of said device; and

control means for starting excitation of the stepping motor from said final exciting phase stored in said storage means at the time of restarting from a power off state of said device.

13. The recording apparatus according to claim 12, comprising:

phase alignment means for aligning a mechanical phase of said stepping motor and an electrical phase stored in said storage means, wherein

at the time of restarting from the power off state of said device, when said control means starts the excitation of the stepping motor from said final exciting phase stored in said storage means, the phase alignment means does not perform the phase alignment.

14. The recording apparatus according to claim 13, comprising:

another storage means for storing a termination status indicating a presence/absence of abnormality at

the time of power off of said device, wherein

at the time of restarting from the power off state of said device, said control means starts the excitation of the stepping motor from said final exciting phase stored in said storage means without performing the phase alignment by said phase alignment means when said another storage means stores a normal termination status, and performs the phase alignment by said phase alignment means before starting drive of the stepping motor when said another storage means stores an abnormal termination status.

15. The recording apparatus according to claim 14, wherein said control means starts the phase alignment by said phase alignment means from said final exciting phase stored in said storage means when said another storage means stores an abnormal termination status.

16. A recording apparatus provided with a stepping motor as an actuator, comprising:

drive means for changing an exciting phase of the stepping motor to step-drive the stepping motor;

storage means for storing a final exciting phase of said stepping motor at the time of power off of said device;

phase alignment means for aligning a mechanical

phase of said stepping motor and an electrical phase stored in said storage means;

a driven member driven by said stepping motor;

a sensor for judging whether said driven member moves by predetermined pulses from a standby position of the member; and

control means for starting excitation of the stepping motor from said final exciting phase stored in said storage means to drive said driven member by the predetermined pulses at the time of restarting from a power off state of said device, normally starting the stepping motor without performing the phase alignment by said phase alignment means when said sensor judges that the driven member is moved by the predetermined pulses, and performing the phase alignment by said phase alignment means when said sensor judges that the driven member is not moved by the predetermined pulses.

17. A recording apparatus provided with a stepping motor as an actuator, comprising:

drive means for changing an exciting phase of the stepping motor to step-drive the stepping motor;

a sensor for detecting a value corresponding to a rotating amount of said stepping motor even at the time of power off of said device;

storage means for storing a final exciting phase of said stepping motor at the time of power off of said

device, a value stored in the storage means being rewritten in accordance with the value of said sensor at the time of power off; and

control means for, at the time of restarting from a power off state of said device, starting excitation of the stepping motor from the exciting phase rewritten and stored in said storage means also at the time of power off of said device.

ABSTRACT OF THE DISCLOSURE

The present invention provides a recording apparatus provided with a stepping motor as an actuator, comprising storage means for storing and holding a last exciting phase of the motor at the time of software power off and control means for, at the time of restarting from a software power off state, starting excitation from the final exciting phase without performing phase alignment of the motor, wherein starting can constantly be performed from a state in which the electrical and mechanical phases of the motor are equal to each other, so that the generation of noise or slight vibration can be minimized.

D4005.1/0005

FIG. 1

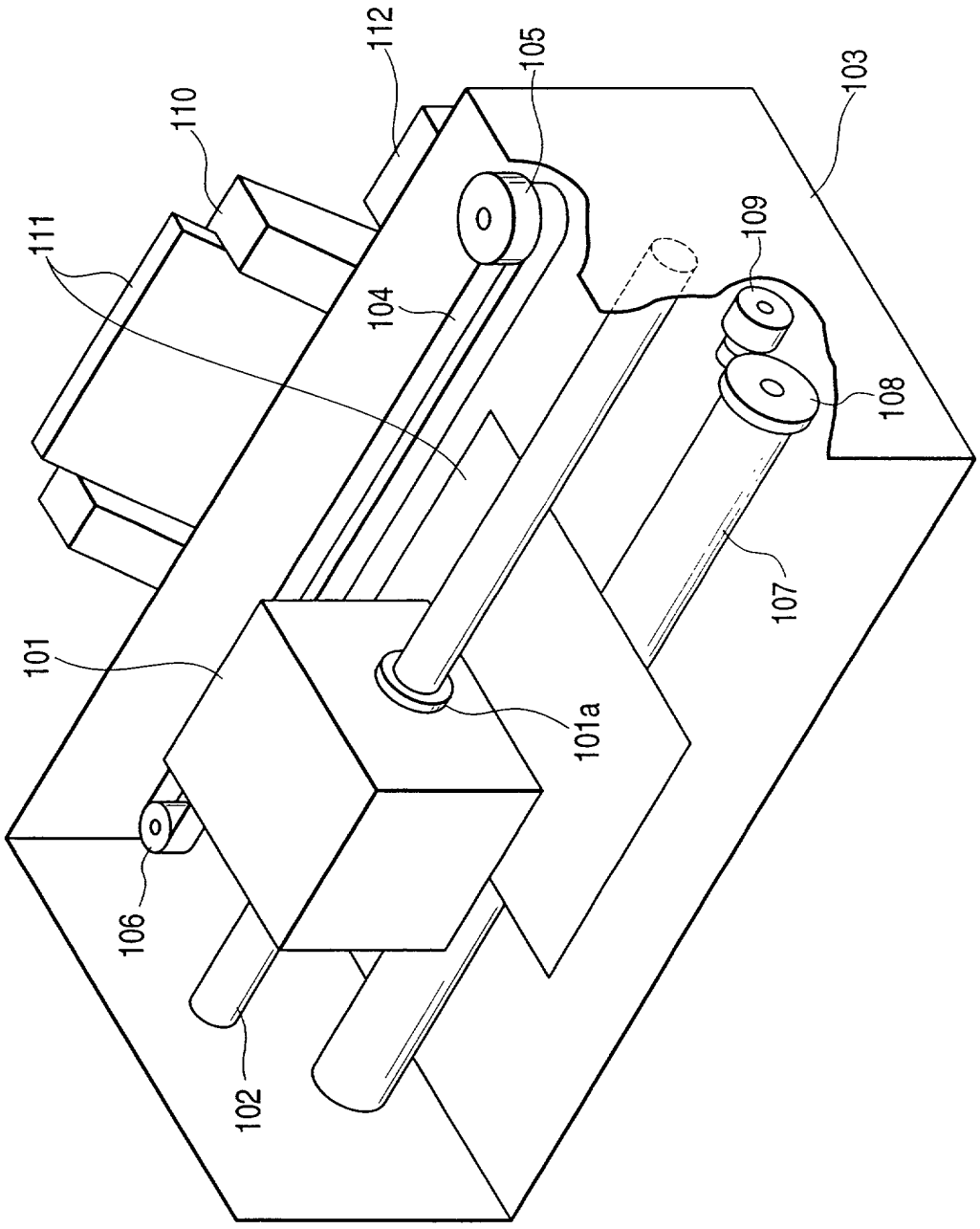


FIG. 2

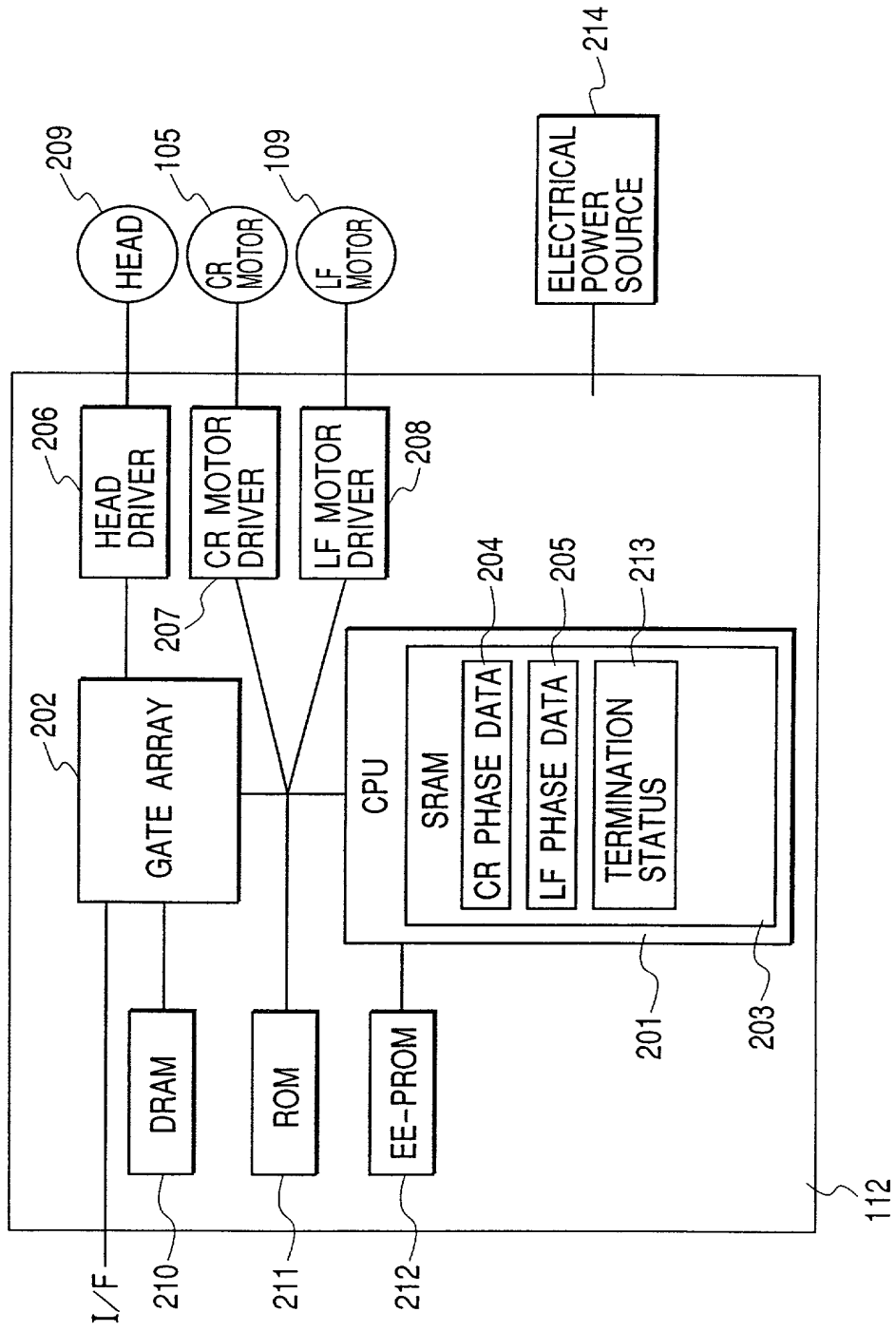

209
HEAD
105
CR MOTOR
109
LF MOTOR
214
ELECTRICAL POWER SOURCE
206
HEAD DRIVER
CR MOTOR DRIVER
LF MOTOR DRIVER
208
207
204
205
213
202
GATE ARRAY
CPU
SRAM
CR PHASE DATA
LF PHASE DATA
TERMINATION STATUS
DRAM
ROM
EE-PROM
201
203
I/F
210
211
212
112

FIG. 3

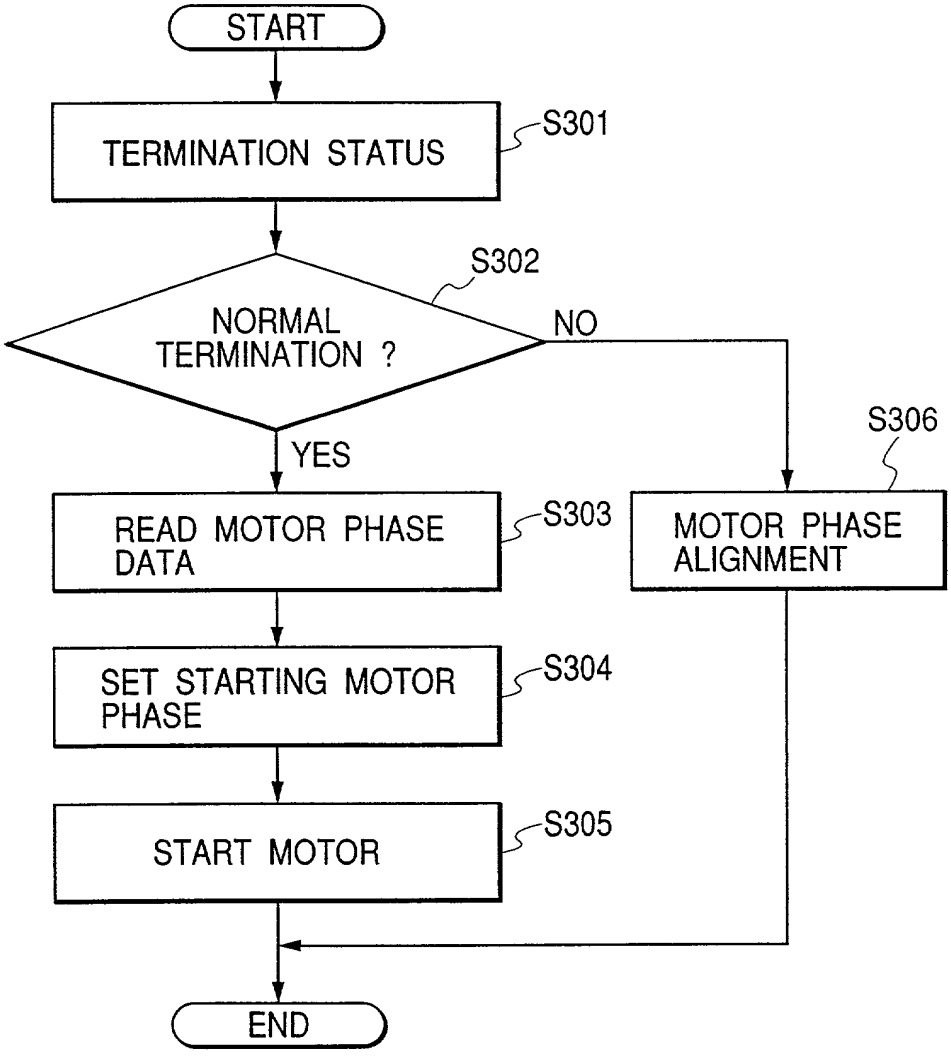

START
TERMINATION STATUS
S301
S302
NORMAL TERMINATION ?
NO
YES
S306
READ MOTOR PHASE DATA
S303
MOTOR PHASE ALIGNMENT
SET STARTING MOTOR PHASE
S304
START MOTOR
S305
END

FIG. 4

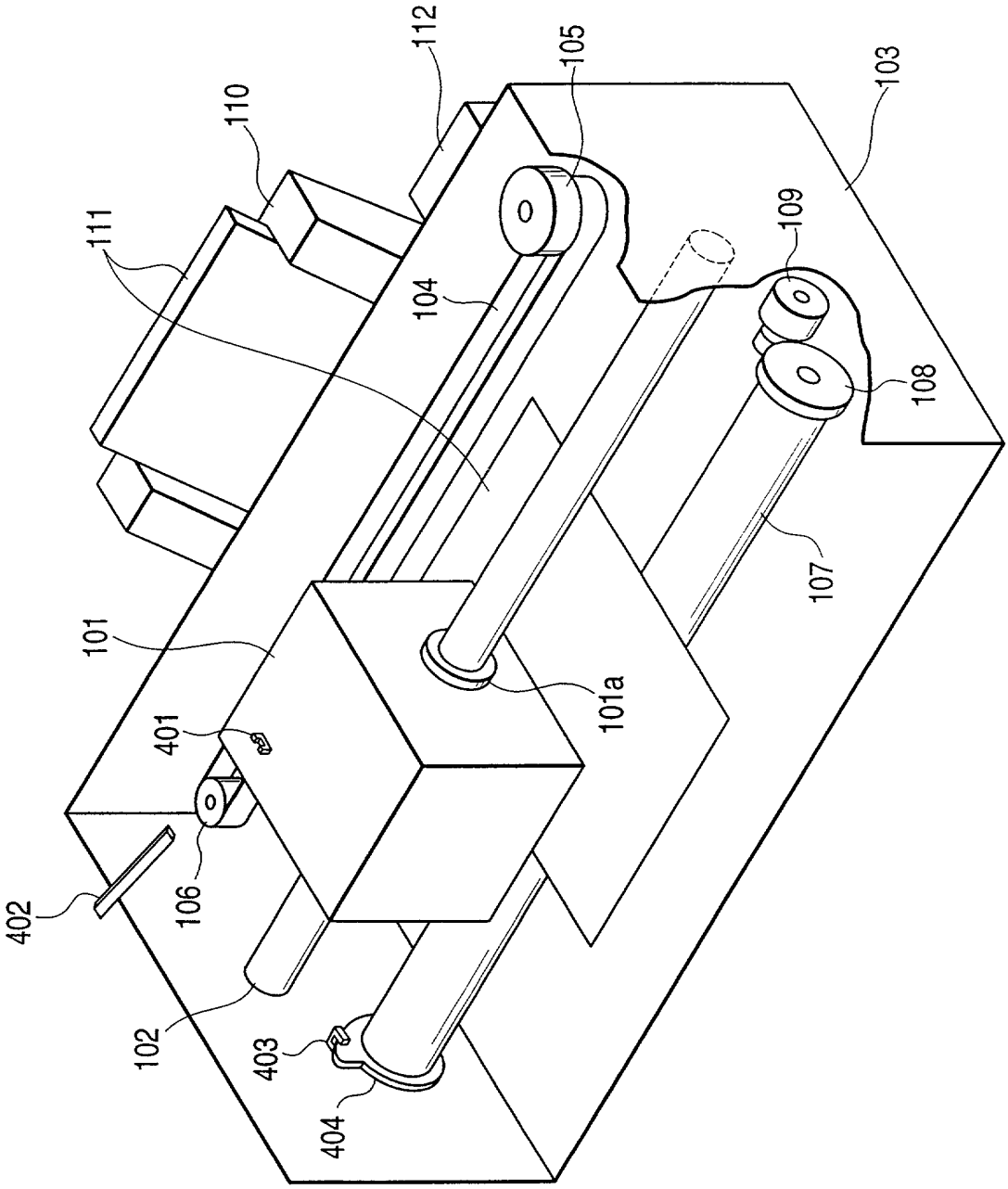

112
105
103
110
111
109
104
108
101
107
401
101a
402
106
102
403
404

FIG. 5

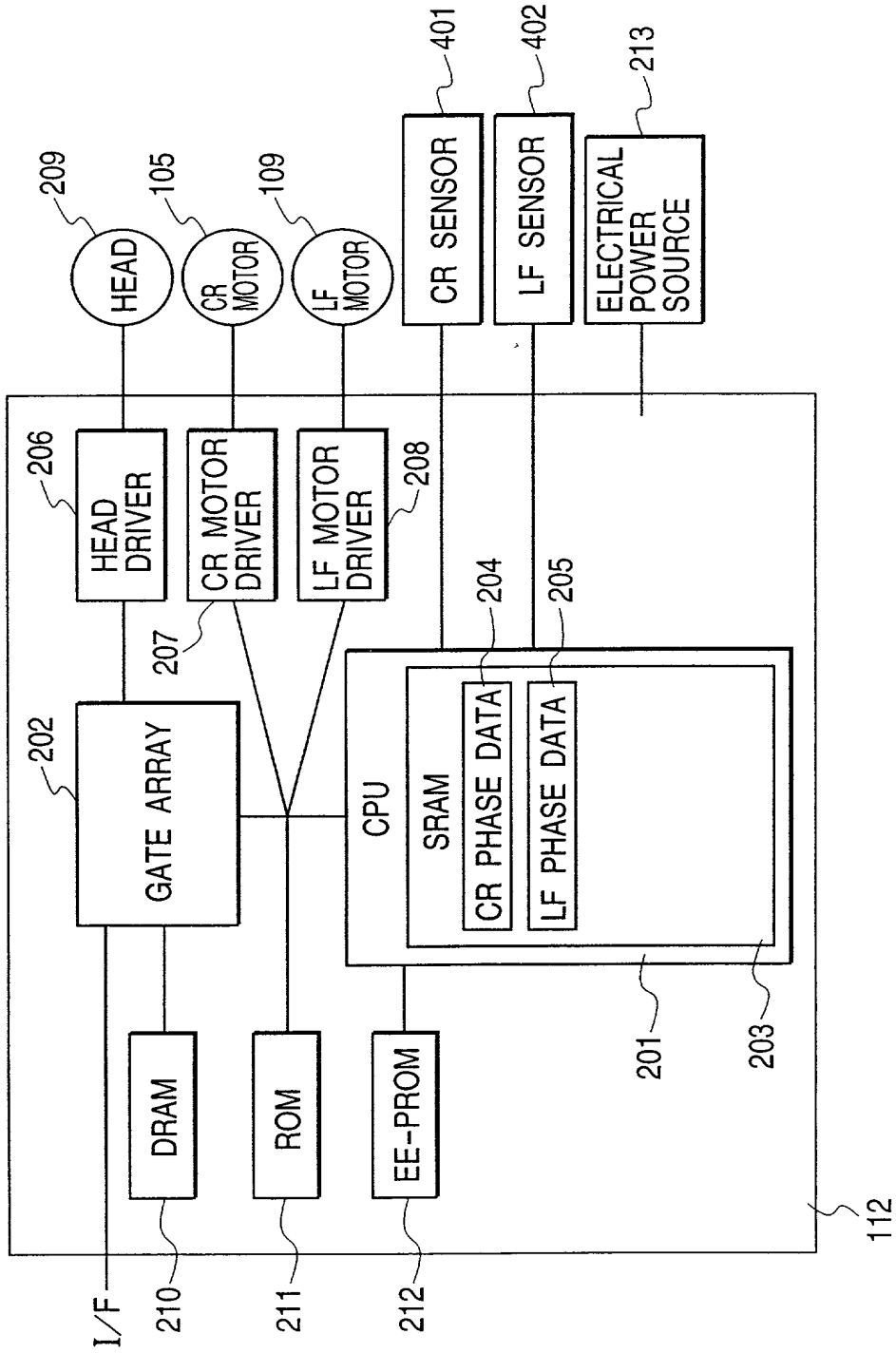

209
HEAD
105
CR MOTOR
109
LF MOTOR
401
CR SENSOR
402
LF SENSOR
213
ELECTRICAL POWER SOURCE
206
HEAD DRIVER
CR MOTOR DRIVER
LF MOTOR DRIVER
208
207
204
205
202
GATE ARRAY
CPU
SRAM
CR PHASE DATA
LF PHASE DATA
DRAM
ROM
EE-PROM
201
203
I/F
210
211
212
112

FIG. 6

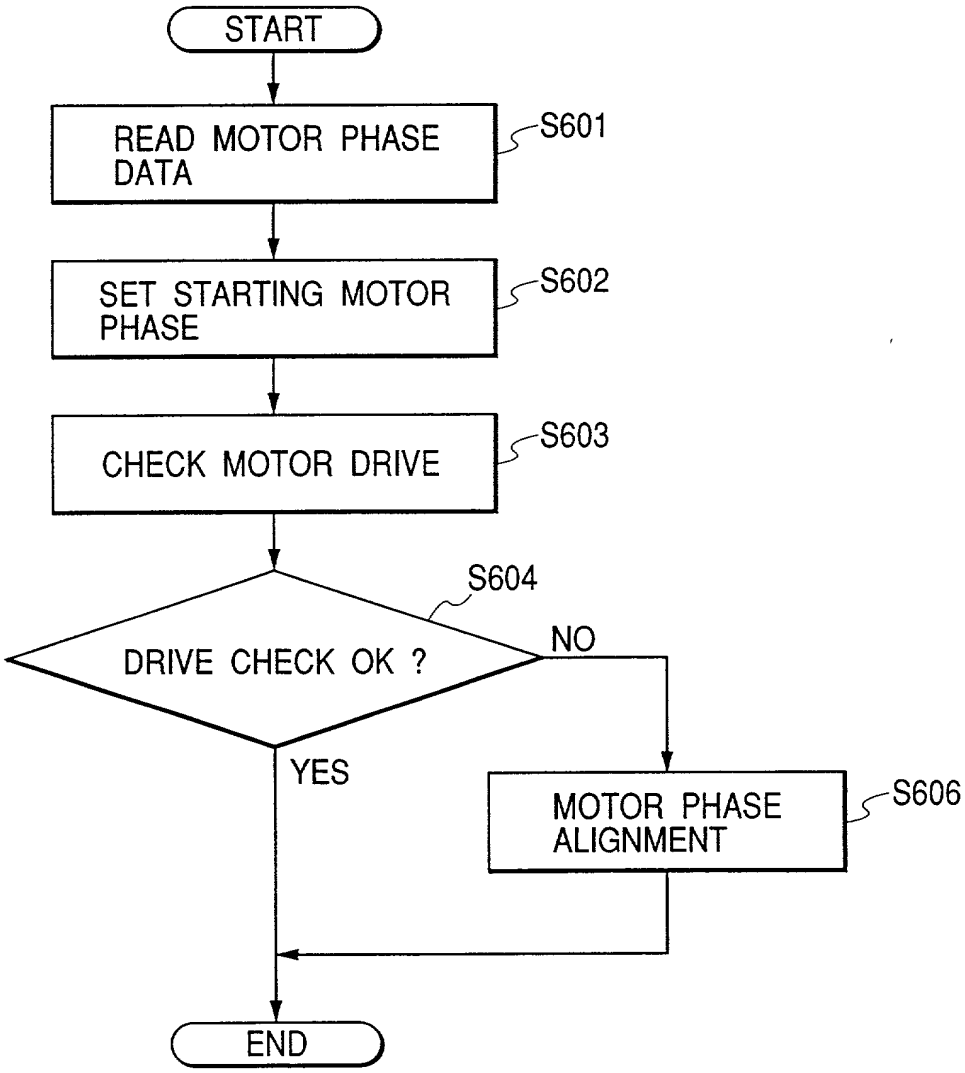

START
READ MOTOR PHASE DATA
S601
SET STARTING MOTOR PHASE
S602
CHECK MOTOR DRIVE
S603
S604
DRIVE CHECK OK ?
NO
YES
MOTOR PHASE ALIGNMENT
S606
END

FIG. 7

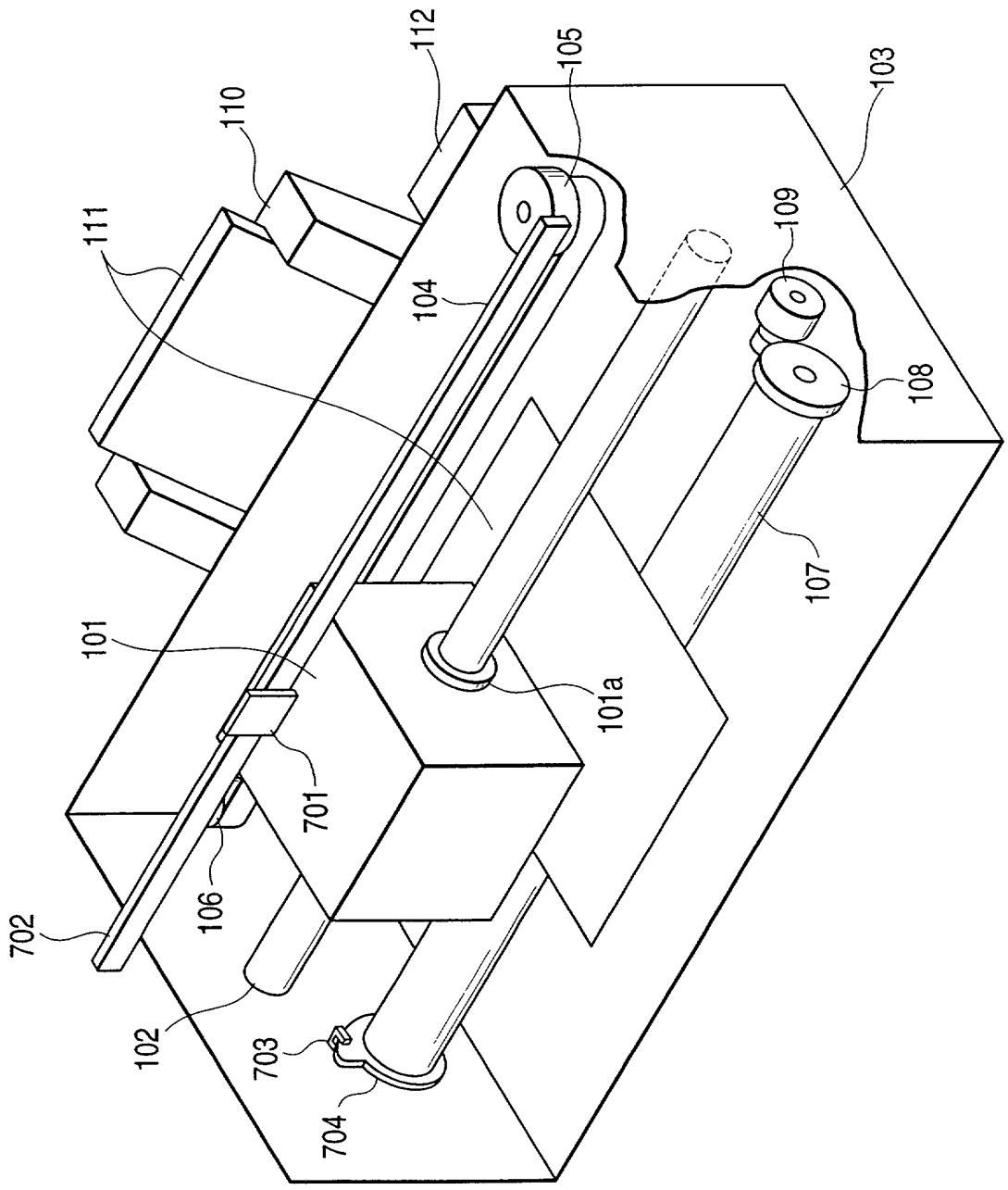

112
105
103
110
111
109
104
108
101
101a
107
701
106
702
102
703
704

FIG. 8

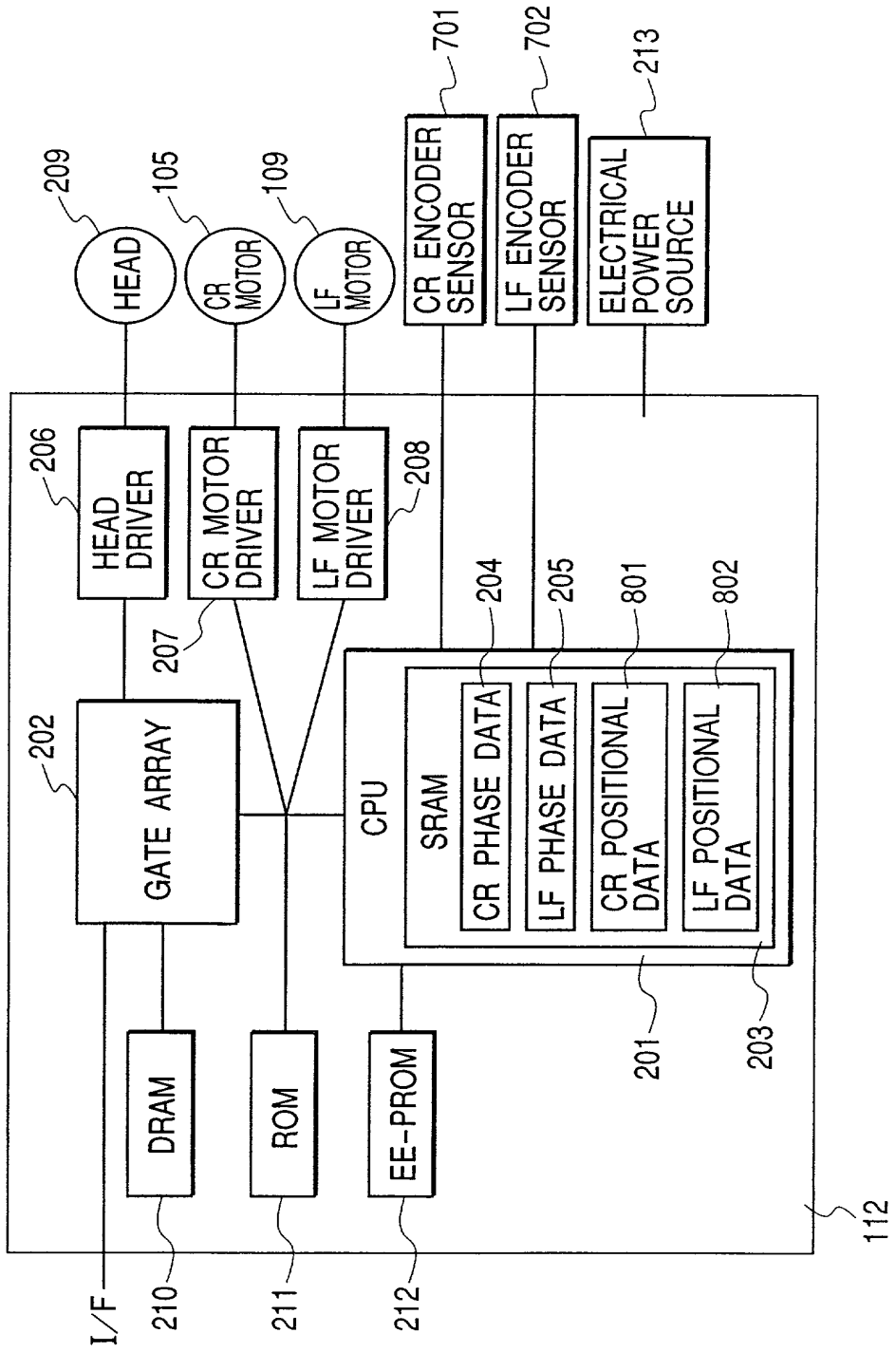

HEAD
CR MOTOR
LF MOTOR
CR ENCODER SENSOR
LF ENCODER SENSOR
ELECTRICAL POWER SOURCE
HEAD DRIVER
CR MOTOR DRIVER
LF MOTOR DRIVER
GATE ARRAY
CPU
SRAM
CR PHASE DATA
LF PHASE DATA
CR POSITIONAL DATA
LF POSITIONAL DATA
DRAM
ROM
EE-PROM
I/F
209
105
109
701
702
213
206
208
207
202
204
205
801
802
201
203
210
211
212
112

FIG. 9

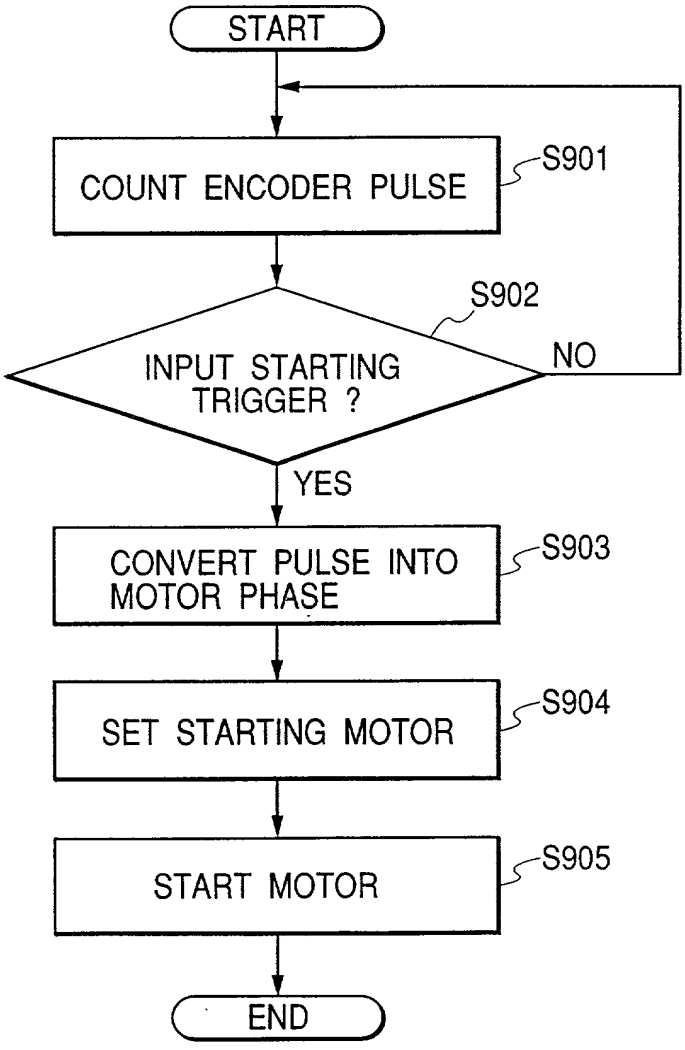

START
COUNT ENCODER PULSE
S901
INPUT STARTING TRIGGER ?
S902
NO
YES
CONVERT PULSE INTO MOTOR PHASE
S903
SET STARTING MOTOR
S904
START MOTOR
S905
END

FIG. 10A

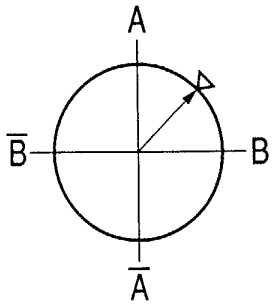


FIG. 10B

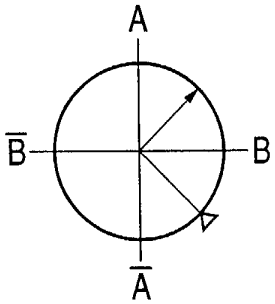


FIG. 10C

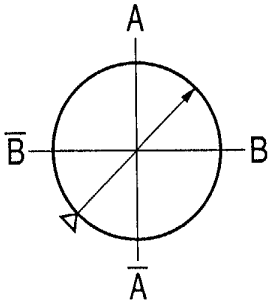
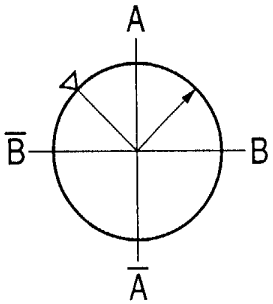


FIG. 10D



CFO 13942 US

COMBINED DECLARATION AND POWER OF ATTORNEY
FOR PATENT APPLICATION
(Page 1)

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name;

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled RECORDING APPARATUS,

the specification of which [X] is attached hereto [ ] was filed on _____ as United States Application No. or PCT International Application No. _____ and was amended on _____ (if applicable).

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to patentability as defined in 37 CFR §1.56.

I hereby claim foreign priority benefits under 35 U.S.C. §119(a)-(d) or §365(b), of any foreign application(s) for patent or inventor's certificate, or § 365(a) of any PCT international application which designates at least one country other than the United States, listed below and have also identified below any foreign application for patent or inventor's certificate, or PCT international application having a filing date before that of the application on which priority is claimed:

Country	Application No.	Filed (Day/Mo./Yr.)	(Yes/No) Priority Claimed
Japan	10-321306	26 October 1998	Yes

I hereby appoint the practitioners associated with the firm and Customer Number provided below to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith, and direct that all correspondence be addressed to the address associated with that Customer Number:

FITZPATRICK, CELLA, HARPER & SCINTO
Customer Number: 05514

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

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Full Name of Second Joint Inventor, if any _____

Second Inventor's signature _____

Date _____ Citizen/Subject of _____

Residence _____

Post Office Address _____